

FIG 5

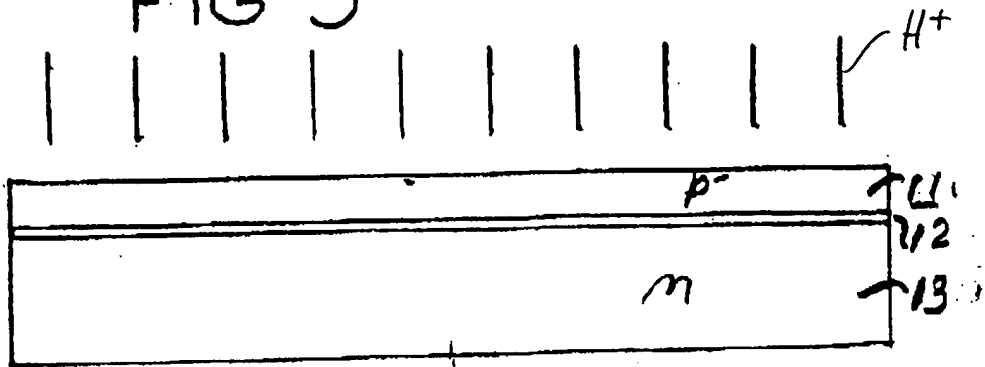


FIG 6

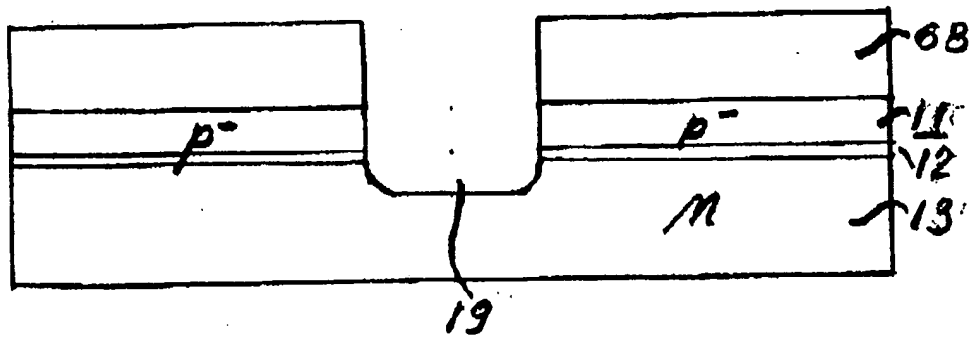
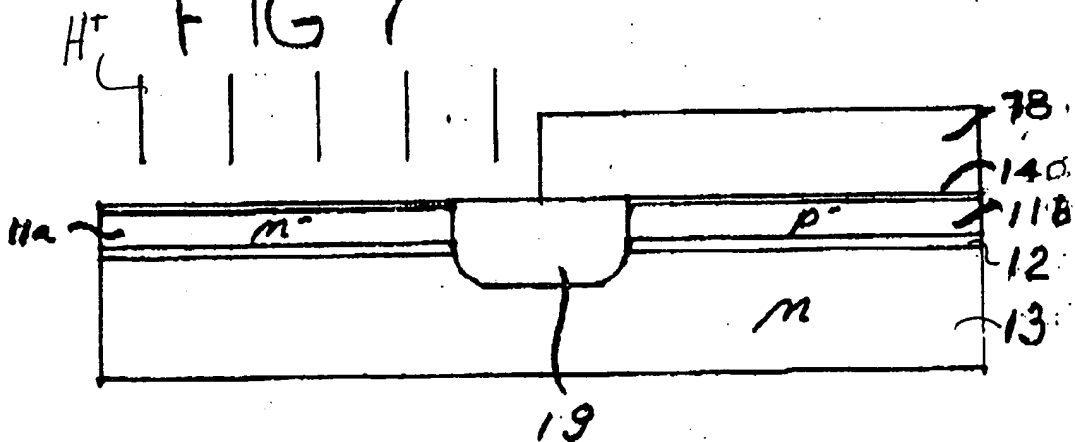
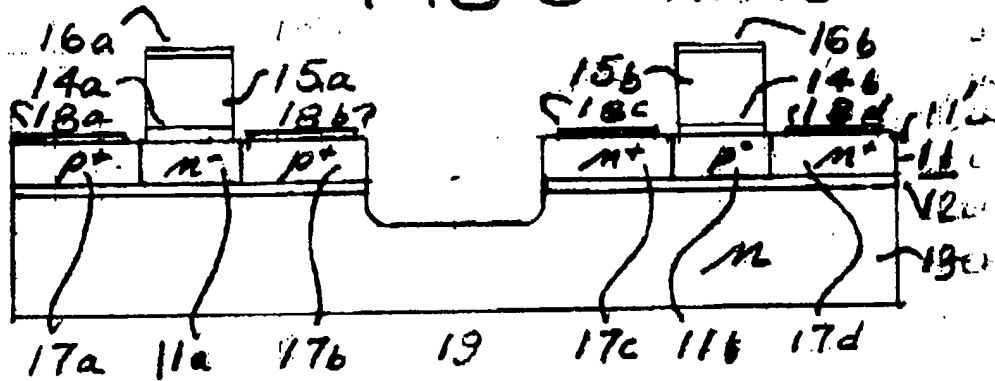


FIG 7



[illegible]

PMOS      FIG 8      NMOS



PMOS      FIG 9      NMOS

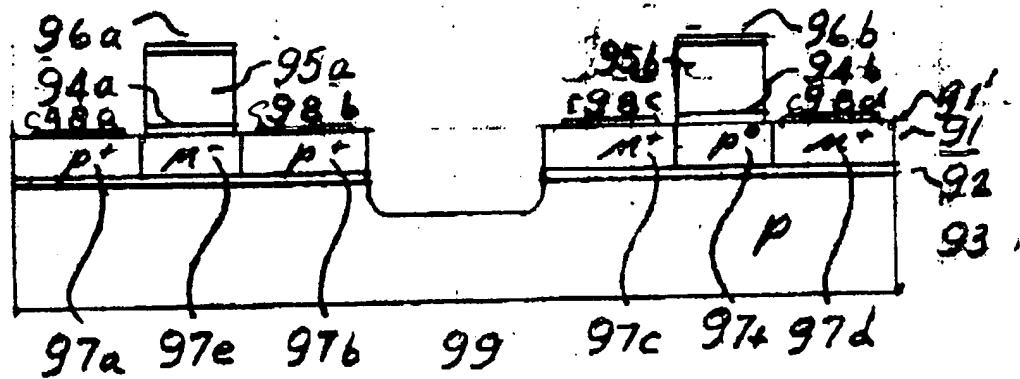


FIG 4

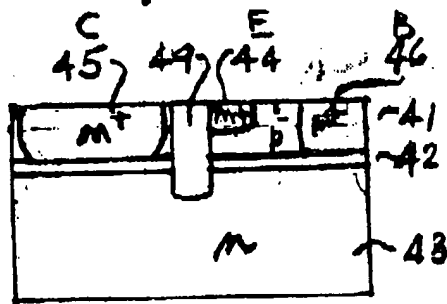


FIG 10

